

S/N 09/618,648PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Joseph E. Geusic et al. Examiner: Wai-Sing Louie  
Serial No.: 09/618,648 ✓ Group Art Unit: 2814  
Filed: July 18, 2000 Docket: 303.382US2  
Title: INTEGRATED CIRCUITS USING OPTICAL WAVEGUIDE  
INTERCONNECTS FORMED THROUGH A SEMICONDUCTOR WAFER  
AND METHODS FOR FORMING SAME

EXT-1  
#11  
Chen  
7/29/02

PETITION FOR A ONE-MONTH EXTENSION OF TIME

BOX AF  
Commissioner for Patents  
Washington, D.C. 20231

In accordance with the provisions of 37 C.F.R. § 1.136(a), it is respectfully requested that a one-month extension of time be granted in which to respond to the Final Office Action mailed March 21, 2002, said period of response being extended from June 21, 2002 to July 21, 2002.

Please charge Deposit Account No. 19-0743 in the amount of \$110.00 to cover the required extension fee.

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Respectfully submitted,

JUL 22 2002

JOSEPH E. GEUSIC ET AL.

TECHNOLOGY CENTER 2800

By their Representatives,

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07/29/2002 GLENTS 00000002 199743 09618648  
01 FD:115 110.00 CH

Date

7/22/2002

By

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I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on the date shown below.

Tina M. Kohout

7/22/02  
Date of Transmission